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Solution: (a) Instruction Cycle Time Is  $1/(4 \text{ MHz}) = 250 \text{ Ns}$  (b) Instruction Cycle Time Is  $1/(8 \text{ MHz}) = 125 \text{ Ns}$  (c) Instruction Cycle Time Is  $1/(10 \text{ MHz}) = 100 \text{ Ns}$  Example 8-1. Fuse Bits And Reset Delay The Most Difficult Time For A System Is During Power-

up. The CPU Needs 7th, 2021

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## **EMBEDDED SYSTEMS DESIGN**

Most Embedded Systems Are Built On Microcontrollers, Which Run Faster Than A Custom-built System With A Microprocessor, Because All Components Are Integrated Within A Single Chip. Operating System Plays An Important Role In Most Of The Embedded Systems. But All The Embedded Systems Do Not Use The Operating System. 10th, 2021

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